Docket No.: M-9351 US
Title: "Method Of Programming An Antifuse"
Inventors: Rajiv Jain and Richard J. Wong
Contact: Michael Halbert at 408/453-9200, 1209

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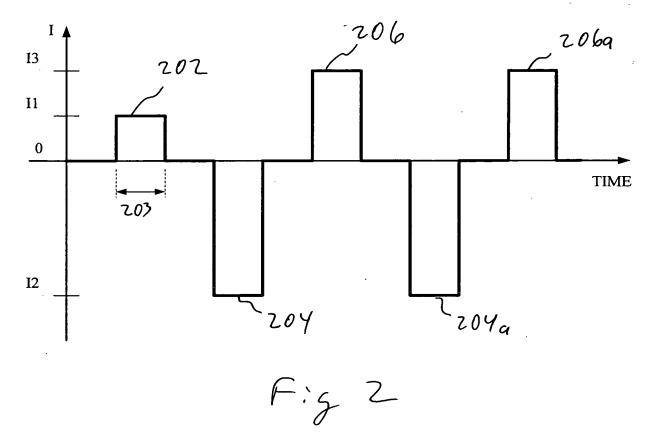
Fig 1

Fig 2

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100

112

113

106

114

108

110

104

104

102

Fig 3

100   113a

114

112

104   110

108   104

102
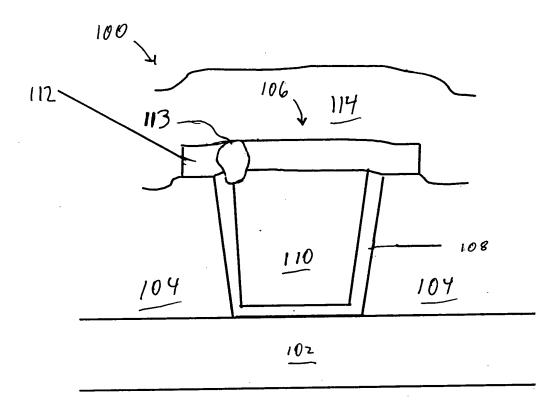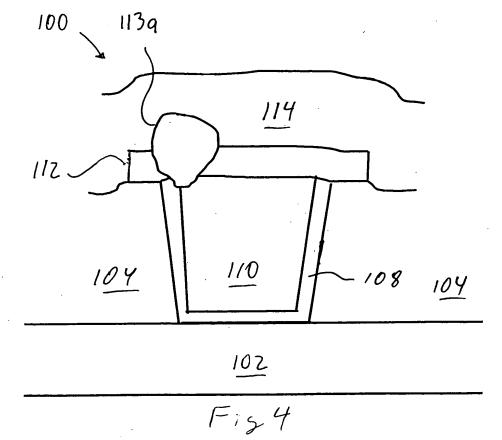
Fig 4

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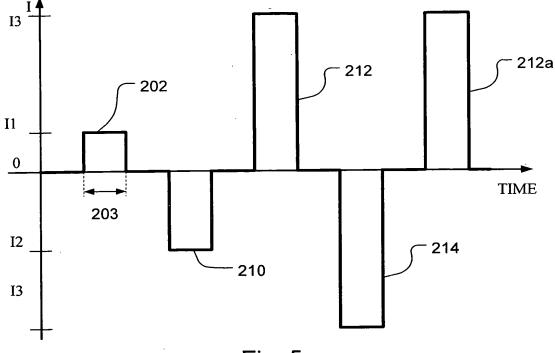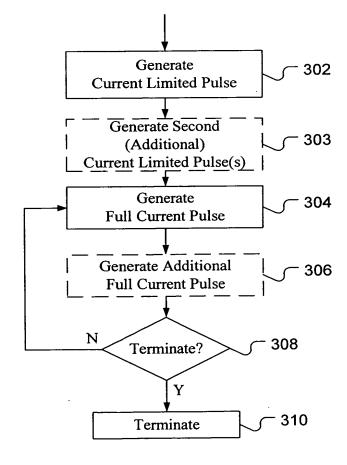Contact: Michael Halbert at 408/453-9200, 1209

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Fig. 5

Current pulse diagram showing vertical axis I with levels I3, I1, 0, I2, I3 and horizontal axis TIME. Pulses labeled 202 (with width 203), 212, 212a, 210, 214.

Fig. 6

Flowchart:

- Generate Current Limited Pulse — 302
- Generate Second (Additional) Current Limited Pulse(s) — 303
- Generate Full Current Pulse — 304
- Generate Additional Full Current Pulse — 306
- Terminate? — 308 (N loops back to 304, Y continues)
- Terminate — 310

320

326

322

321

330

324

Fig 7

+18V

[D2]

RP9
8 2K

2 1
20K
RP17

TND900
OP1 12

TND900
OP1 11

RP17
20K 3

5

6

AGND

AGND

9   11

LM324
8

3

[DAC1C]

10

2   QD8

OP1

2N4402

[OCUR2]

_1,1,1DL2 +24V

R6
47

AGND

Fig 10

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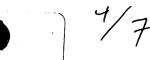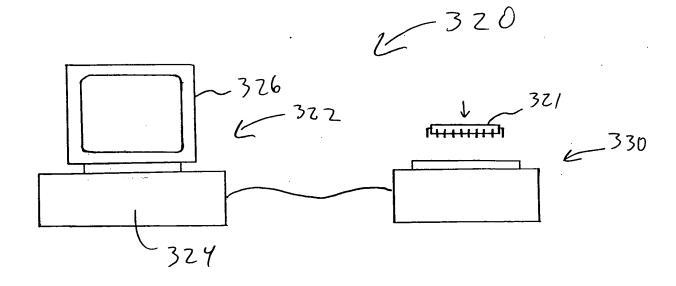Contact: Michael Halbert at 408/453-9200, 1209

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Fig 8
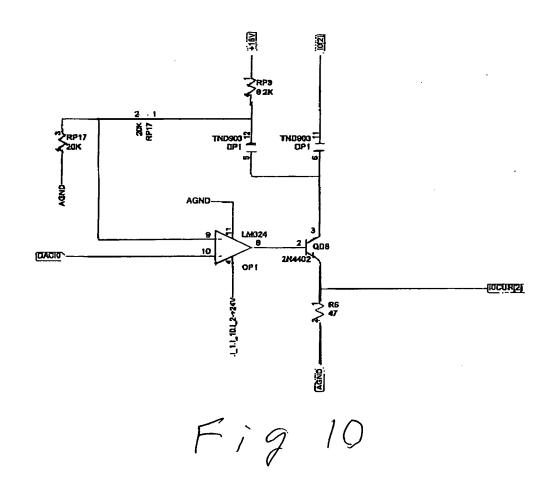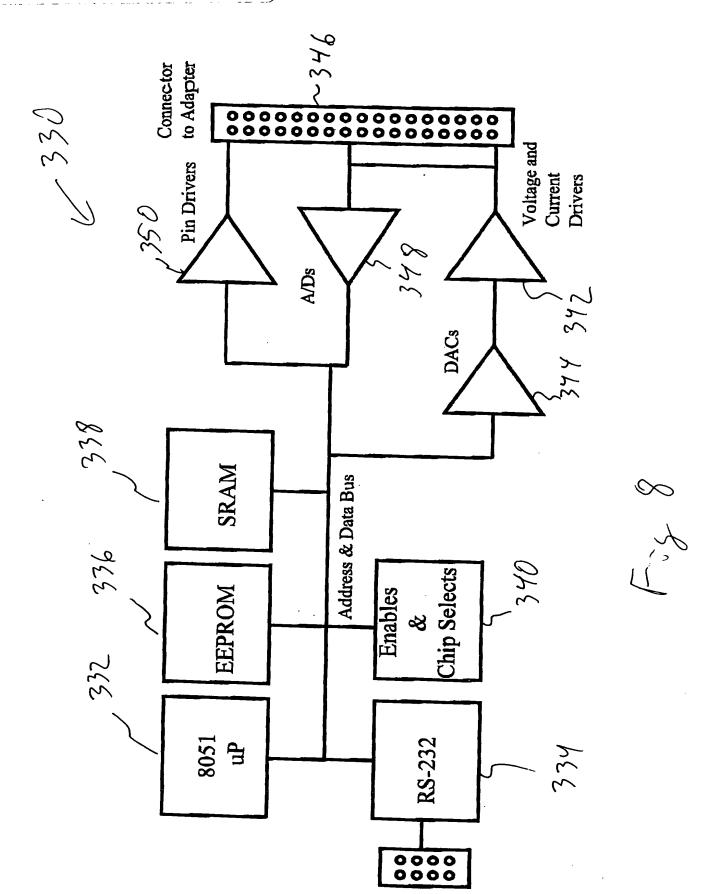
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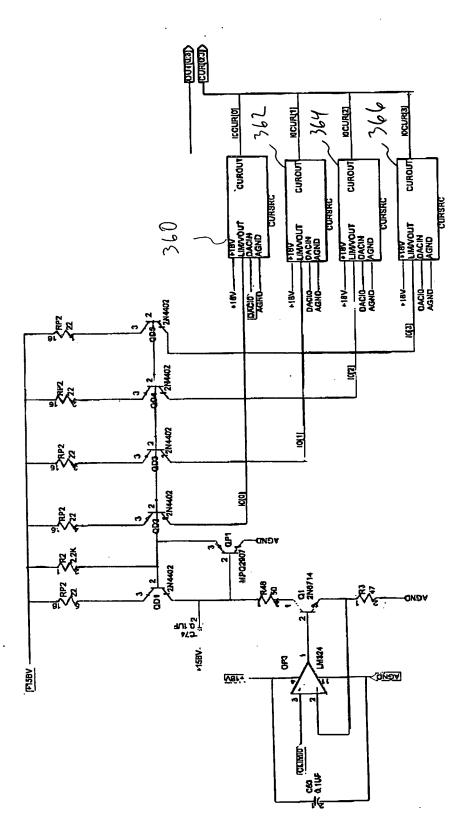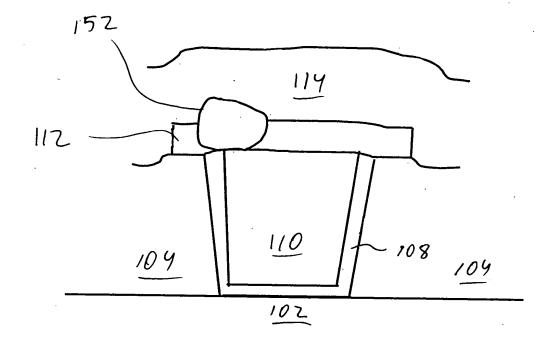
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Fig. 9

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152

114
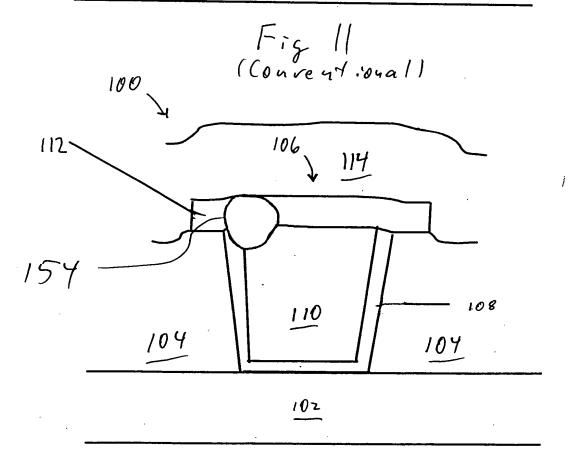
112

110

104   108   104

102

Fig 11
(Conventional)

100

112

106   114

154

110

104   108

104

102

Fig 12
(Conventional)